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			Application Number		09/770,478		
	TRANSMITTAL FORM			Filing Date		Jan 29, 2001	
-				First Named Inventor		Del Signore, James R.	
(to be used for all correspondence after initial filing)			Group Art Unit		2816		
			er initial filing)	Examiner Name		Quan Tra	
Total Number of Pages in This Sul			bmission	Attorney Docket Number		230P051	
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	Fee Attached		Drawing(s)			Appeal Communication to Board of Appeals and Interferences	ľ
À	Amendment / Reply		Licensing-related Papers			Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)	
	After Final		Petition			Proprietary Information	
	Affidavits/declaration(s)		Petition to Convert to a Provisional Application			Status Letter	
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	Response to Missing Parts/ Incomplete Application		Remarks			18 2012 HAIL ROOM	키
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Firm George R. McGuire or HANCOCK & ESTABROOK, LLP							
Signature DX-							╡
Date April 9, 2002							7
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## TC 2800 HAIL ROOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

April 8, 2002

In re the application of:

James R. Del Signore II et al.

Filed: January 29, 2001

Ser. No. 09/770,478

For: Current Inrush Limiting Circuit

Docket No. 230 P 051

Art Unit: 2816

Examiner: Quan Tra

Confirmation No. 6823

## **AMENDMENT**

Hon. Commissioner of Patents And Trademark Washington D.C. 20231

Sir:

In response to the Official Action of January 9, 2002, please amend the application as follows:

## Please amend claim 1 to read as follows:

1 1. An inrush circuit for electronic devices having high input capacitance, said inrush 2 circuit comprising: 3 means for providing a voltage ramp; a. means defining an output voltage; b. 5 an operational amplifier circuit having a reference input, said c. 6 operational amplifier circuit receiving said voltage ramp at said 7 reference input and comparing a divided sample of said output voltage {H0095277.1}